

Low Voltage Precision Adjustable Shunt Regulator

FEATURES

- Tight Tolerance..... 0.5% @ 10mA
- Wide Operating Range..... 1mA to 100mA
- Low Extended Temperature Range 0°C to 105°C
- Low Temperature Coefficient 30 ppm/°C
- Offered in TO-92, SOT-89, SOT-23-3 and SO-8
- Improved replacement in performance of AS431 & TL431
- Low Cost Solution

APPLICATIONS

- Battery Operating Equipment
- Adjustable Supplies
- Switching Power Supplies
- Error Amplifiers
- Single Supply Amplifier
- Monitors/ VCR/ TV
- Personal Computers

PRODUCT DESCRIPTION

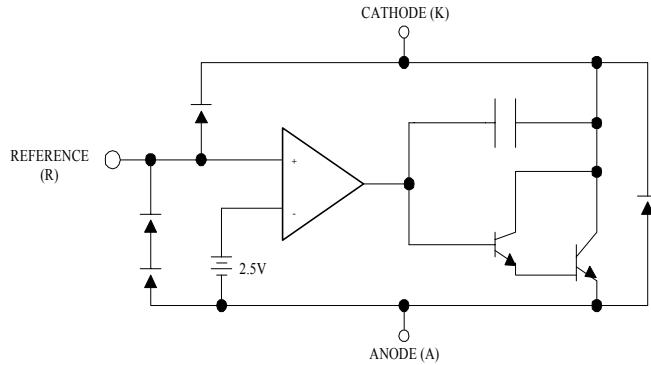
The AS431L is a 3-terminal Adjustable Shunt Voltage Regulator providing a highly accurate bandgap reference. AS431L acts as an open-loop error amplifier with a 2.5V temperature compensation reference. The AS431L thermal stability, wide operating current (100mA) and temperature range (0°C to 105°C) makes it suitable for all variety of applications that are looking for a low cost solution with high performance.

The output voltage may be adjusted to any value between V_{REF} and 20V with 2 external resistors. The AS431L is operating in full industrial temperature range of 0°C to 105°C. The AS431L is available in TO-92, SOT-89, SOT-23-3 and SO-8 packages.

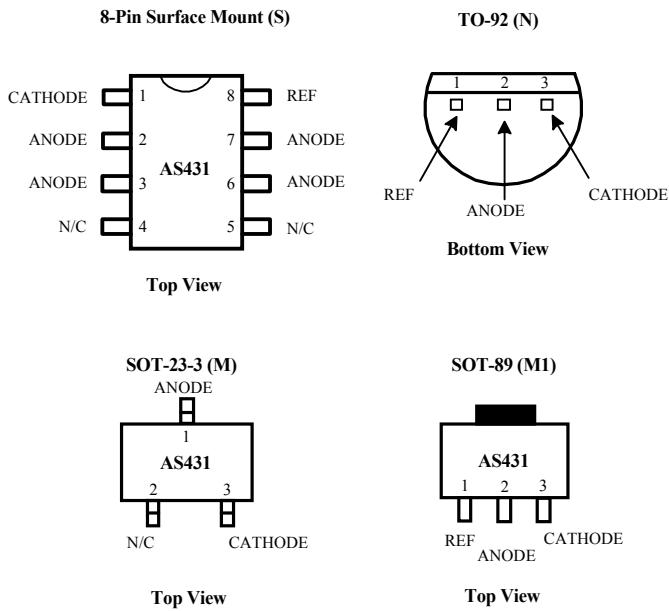
ORDERING INFORMATION

Part Number	Temperature Range	Package Type
AS431LXN	0°C to 105°C	TO-92
AS431LXM1	0°C to 105°C	SOT-89
AS431LXS	0°C to 105°C	SO-8
AS431LXM	0°C to 105°C	SOT-23-3

X= Tolerance (A = 0.5%; Blank = 1%; C = 2%)



PIN CONNECTIONS



ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Rating	Units
Cathode-Anode Reverse Breakdown	V _{KA}	30	V
Anode-Cathode Forward Current	I _{AK}	1	A
Operating Cathode Current	I _{KA}	150	mA
Reference Input Current	I _{REF}	10	mA
Continuous Power Dissipation at 25°C TO-92 SOT-23 SOIC-8 SOT-89	P _D	775 200 750 1000	mW mW mW mW
Junction Temperature	T _J	150	°C
Storage Temperature	T _{STG}	-65 to +150	°C
Lead Temperature (Soldering 10 sec.)	T _L	300	°C

Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

RECOMMENDED CONDITIONS

Parameter	Symbol	Rating	Unit
Cathode Voltage	V _{KA}	V _{REF} to 20	V
Cathode Current	I _K	10	mA

TYPICAL THERMAL RESISTANCE

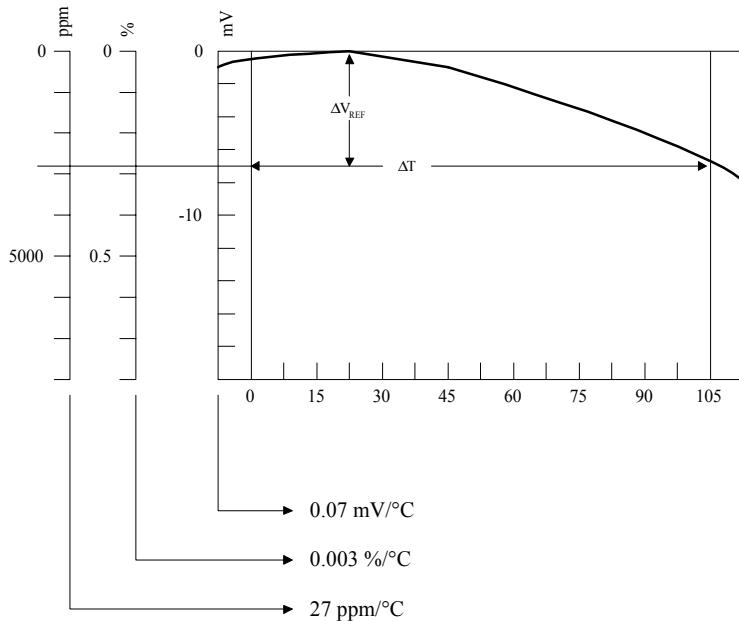
Package Type	θ _{JA}	θ _{JC}	Typical Derating
TO-92	160°C/W	80°C/W	6.3 mW/°C
SOT-23	575°C/W	150°C/W	1.7 mW/°C
SOIC-8	175°C/W	45°C/W	5.7 mW/°C
SOT-89	110°C/W	8°C/W	9.1 mW/°C

ELECTRICAL CHARACTERISTICS at 25°C I_k @ 10mA V_k=V_{ref}, unless otherwise specified.

Parameter	Symbol	Test Conditions	AS431LA			AS431L			AS431LC			Unit
			Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	
Reference Voltage	V _{REF}	T _A = 25°C TC = 1	2.490	2.503	2.515	2.470	2.495	2.520	2.445	2.495	2.545	V
Ratio of Change in V _{REF} to Cathode Voltage	ΔV _{REF} ΔV _K	V _{REF} to 10V 10V to 20V TC = 2	-2.7 -2	-1.0 -0.4	0.3	-2.7 -2	-1.0 -0.4	0.3	-2.7 -2	-1.0 -0.4	0.3	mV/V
Reference Input Current	I _{REF}	TC = 2		0.7	4		0.7	4		0.7	4	μA
I _{REF} Temp Deviation	ΔI _{REF}	Over Temp. TC = 2		0.4	1.2		0.4	1.2		0.4	1.2	μA
Min I _K for Regulation	I _{K(MIN)}	TC = 1		0.4	1		0.4	1		0.4	1	mA
Off State Leakage	I _{K(OFF)}	V _{REF} = 0V, V _{KA} = 20V TC = 3		0.04	1		0.04	1		0.04	1	μA
Dynamic Output Impedance	Z _{KA}	TC = 1		0.15	0.5		0.15	0.5		0.15	0.5	Ω

TC = Test Circuit

*CALCULATING AVERAGE TEMPERATURE COEFFICEINT (TC)

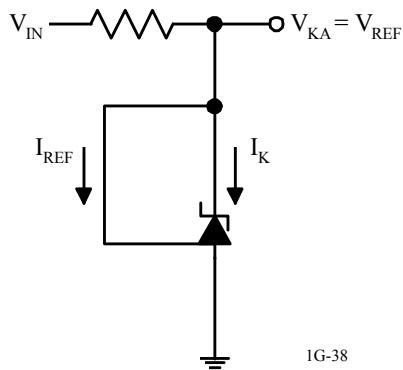


$$\bullet \text{ TC in } \text{mV/}^{\circ}\text{C} = \frac{\Delta V_{\text{REF}} (\text{mV})}{\Delta T_A}$$

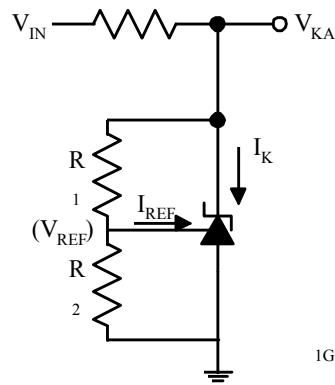
$$\bullet \text{ TC in } \text{mV/}^{\circ}\text{C} = \frac{\left(\frac{\Delta V_{\text{REF}}}{\Delta V_{\text{REF}} \text{ at } 25^{\circ}\text{C}} \right) \times 100}{\Delta T_A}$$

$$\bullet \text{ TC in } \text{ppm/}^{\circ}\text{C} = \frac{\left(\frac{\Delta V_{\text{REF}}}{\Delta V_{\text{REF}} \text{ at } 25^{\circ}\text{C}} \right) \times 10^6}{\Delta T_A}$$

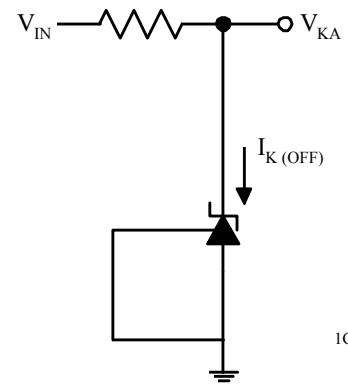
TEST CIRCUITS



1G-38



1G-39



1G-40

TYPICAL PERFORMANCE CURVES

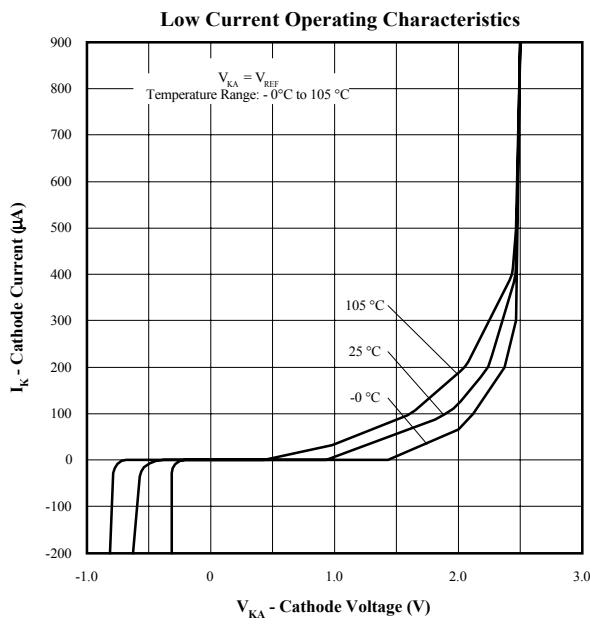


Figure 2

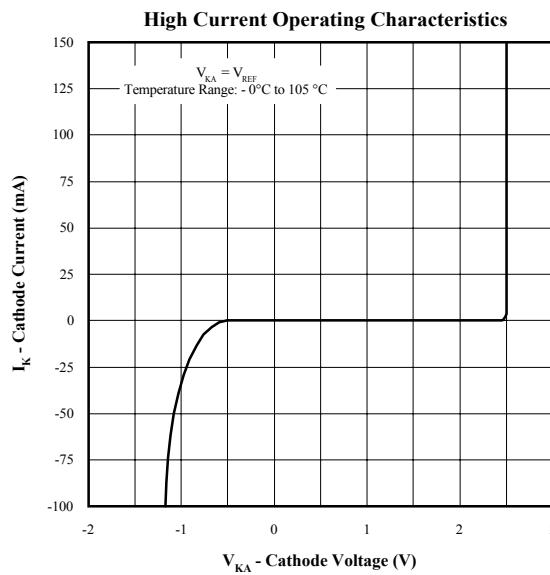


Figure 3

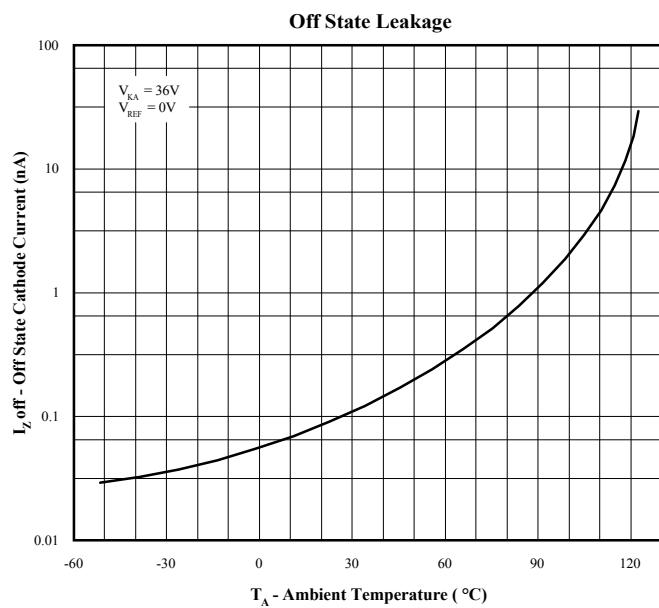


Figure 4

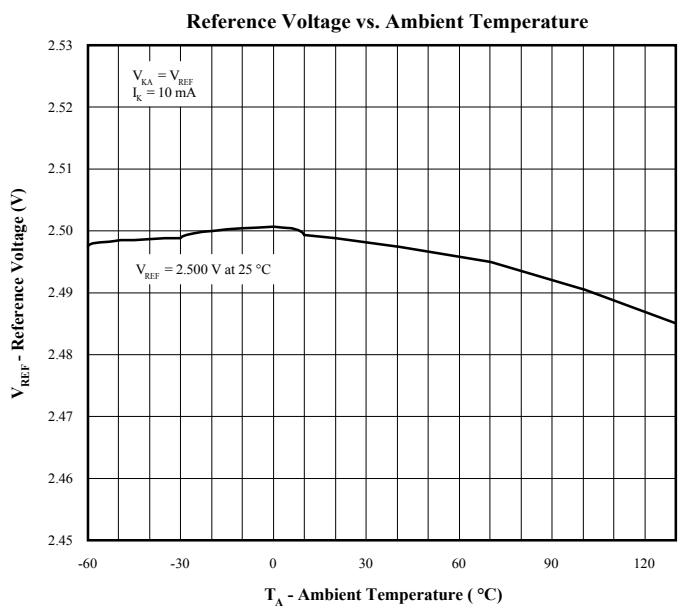


Figure 5

TYPICAL PERFORMANCE CURVES

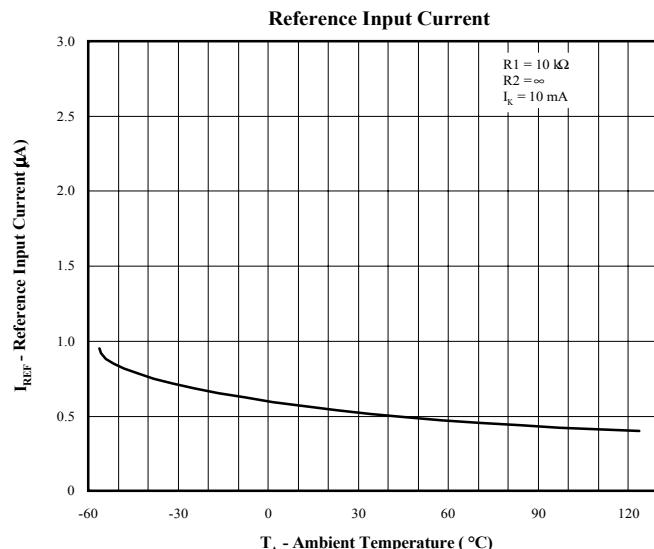


Figure 6

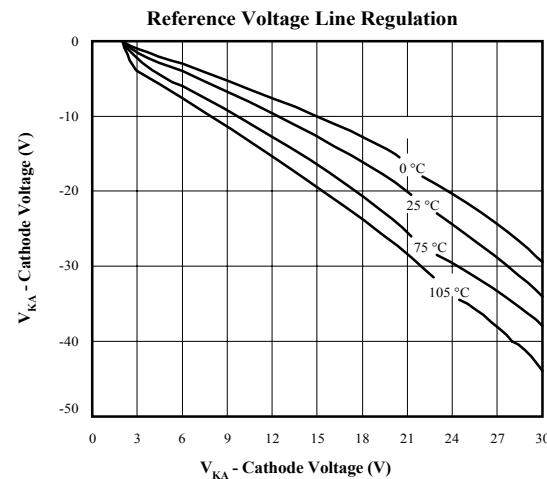


Figure 7

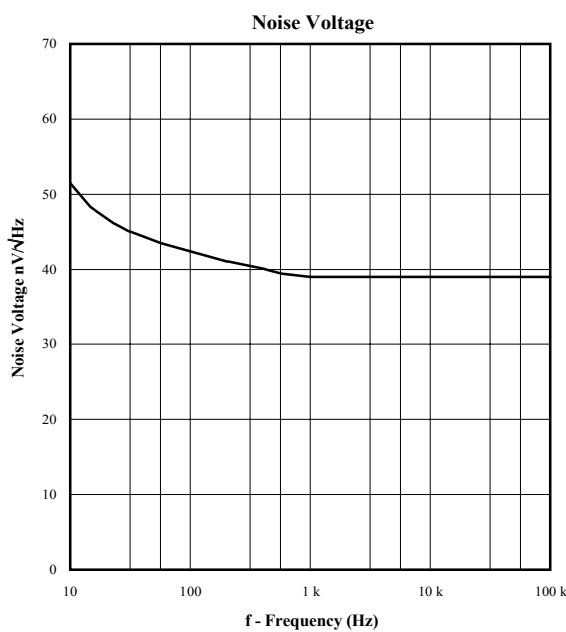


Figure 8

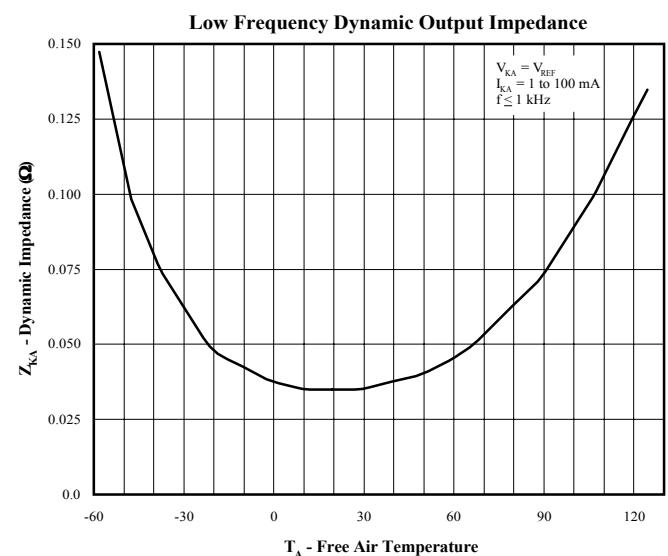


Figure 9

TYPICAL PERFORMANCE CURVES

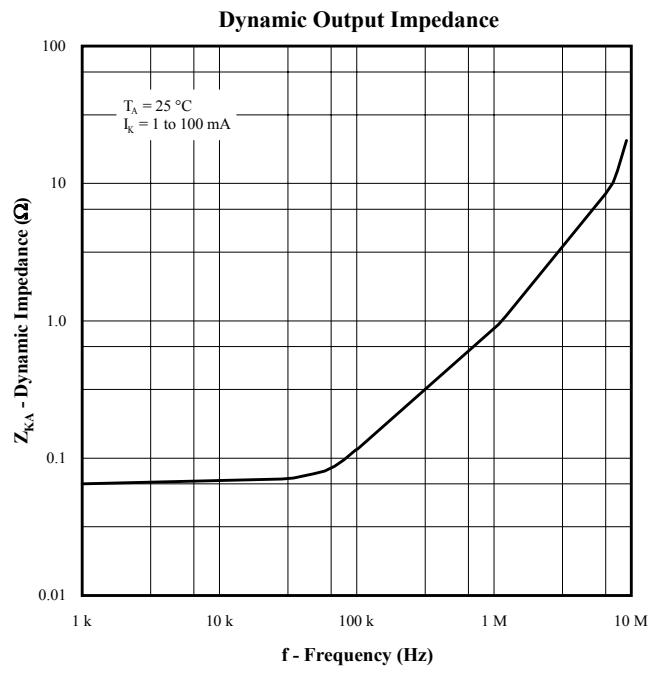


Figure 10

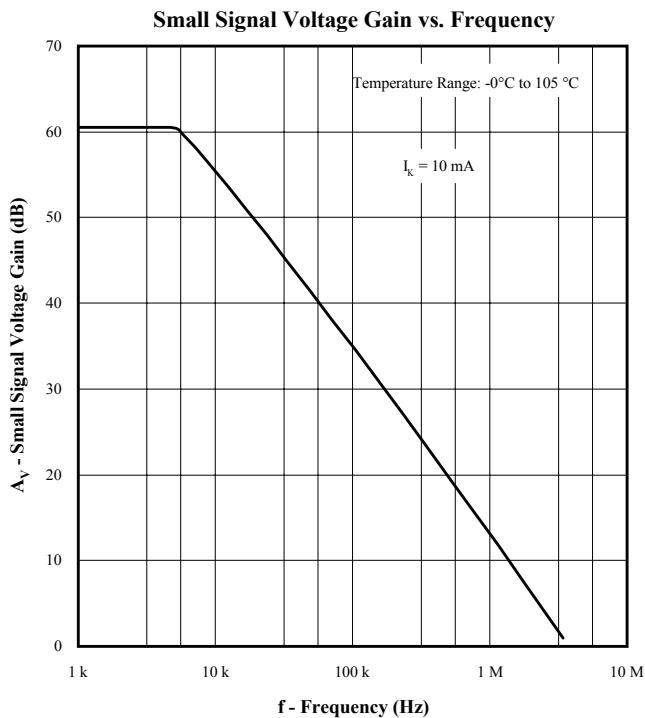
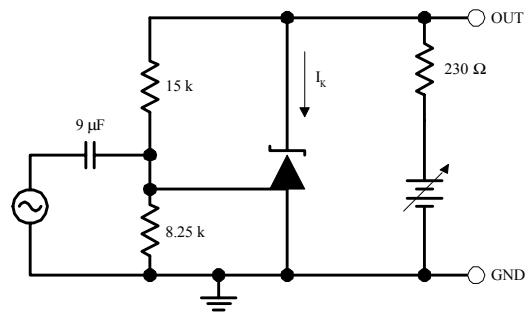


Figure 11



TYPICAL PERFORMANCE CURVES

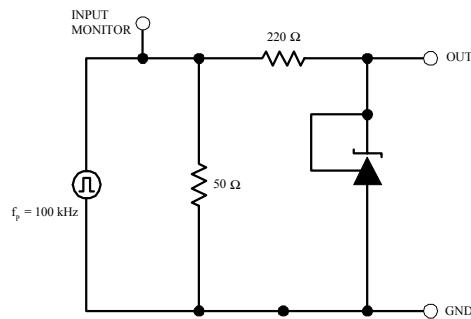
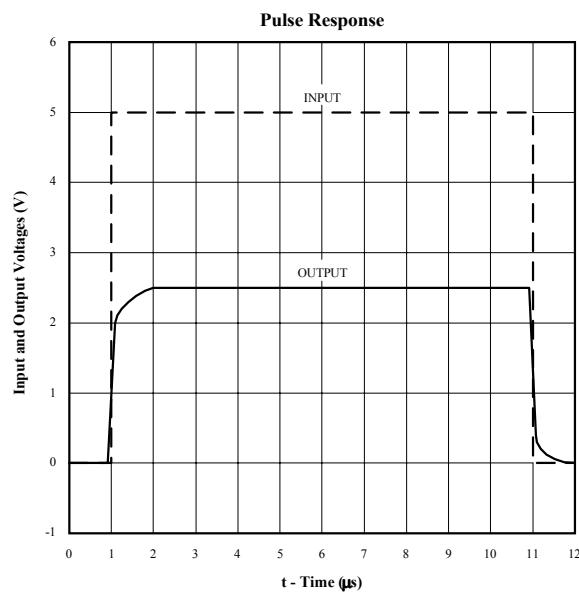


Figure 12

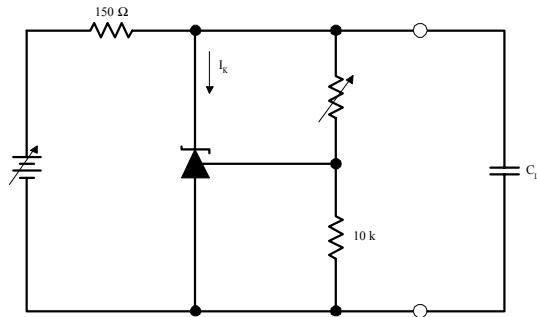
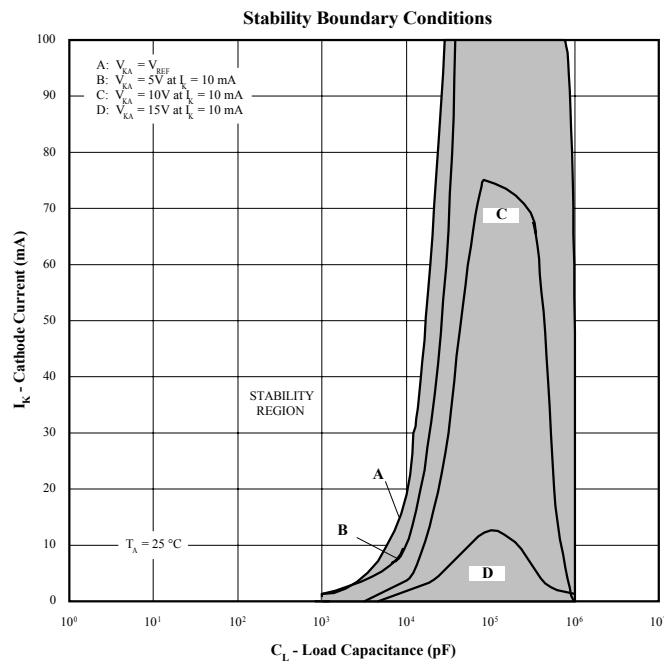


Figure 13