



STM8320

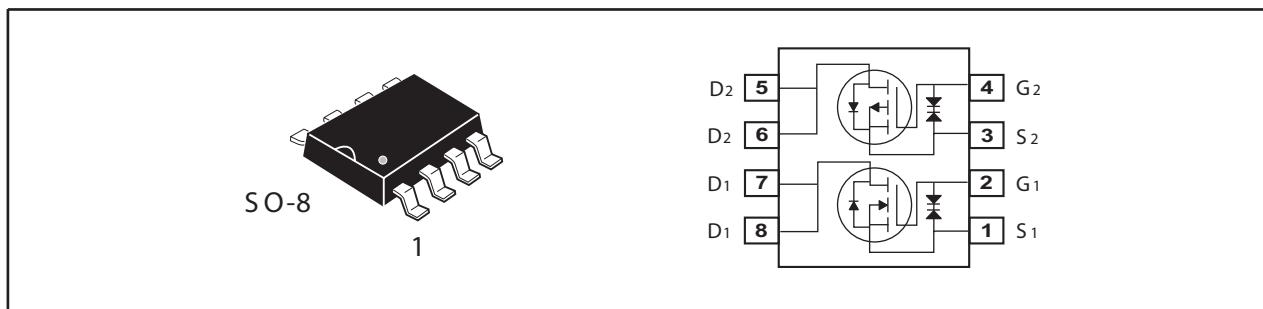
SamHop Microelectronics Corp.

Ver 1.0

Dual Enhancement Mode Field Effect Transistor (N and P Channel)

PRODUCT SUMMARY (N-Channel)		
V _{DSS}	I _D	R _{DSON} (mΩ) Max
30V	7.5A	23 @ V _{GS} =10V
		35 @ V _{GS} =4.5V

PRODUCT SUMMARY (P-Channel)		
V _{DSS}	I _D	R _{DSON} (mΩ) Max
-30V	-6.0A	35 @ V _{GS} =-10V
		55 @ V _{GS} =-4.5V



ABSOLUTE MAXIMUM RATINGS (T_A=25°C unless otherwise noted)

Symbol	Parameter	N-Channel	P-Channel	Units
V _{DS}	Drain-Source Voltage	30	-30	V
V _{GS}	Gate-Source Voltage	±20	±20	V
I _D	Drain Current-Continuous ^a	T _A =25°C	7.5	A
		T _A =70°C	6.0	A
I _{DM}	-Pulsed ^b	27	-22	A
E _{AS}	Single Pulse Avalanche Energy ^d	10	18	mJ
P _D	Maximum Power Dissipation ^a	T _A =25°C	2	W
		T _A =70°C	1.28	
T _J , T _{STG}	Operating Junction and Storage Temperature Range	-55 to 150		°C

THERMAL CHARACTERISTICS

R _{θ JA}	Thermal Resistance, Junction-to-Ambient ^a	62.5	°C/W
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Details are subject to change without notice.

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N-Channel ELECTRICAL CHARACTERISTICS ($T_A=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
OFF CHARACTERISTICS						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{D}}=250\mu\text{A}$	30			V
Id_{SS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=24\text{V}$, $V_{\text{GS}}=0\text{V}$			1	μA
I_{GSS}	Gate-Body Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$			± 10	μA
ON CHARACTERISTICS						
$\text{V}_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$, $I_{\text{D}}=250\mu\text{A}$	1	1.8	3	V
$\text{R}_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance	$V_{\text{GS}}=10\text{V}$, $I_{\text{D}}=7.5\text{A}$		18	23	m ohm
		$V_{\text{GS}}=4.5\text{V}$, $I_{\text{D}}=6.5\text{A}$		26	35	m ohm
g_{FS}	Forward Transconductance	$V_{\text{DS}}=10\text{V}$, $I_{\text{D}}=4.0\text{A}$		14		S
DYNAMIC CHARACTERISTICS ^c						
C_{iss}	Input Capacitance	$V_{\text{DS}}=15\text{V}, V_{\text{GS}}=0\text{V}$ $f=1.0\text{MHz}$		420		pF
C_{oss}	Output Capacitance			130		pF
C_{rss}	Reverse Transfer Capacitance			78		pF
SWITCHING CHARACTERISTICS ^c						
$t_{\text{D}(\text{ON})}$	Turn-On Delay Time	$V_{\text{DD}}=15\text{V}$ $I_{\text{D}}=1\text{A}$ $V_{\text{GS}}=10\text{V}$ $R_{\text{GEN}}=6\text{ ohm}$		8		ns
t_{r}	Rise Time			11		ns
$t_{\text{D}(\text{OFF})}$	Turn-Off Delay Time			40		ns
t_{f}	Fall Time			15		ns
Q_{g}	Total Gate Charge	$V_{\text{DS}}=15\text{V}, I_{\text{D}}=7.5\text{A}, V_{\text{GS}}=10\text{V}$		7.9		nC
		$V_{\text{DS}}=15\text{V}, I_{\text{D}}=7.5\text{A}, V_{\text{GS}}=4.5\text{V}$		4.3		nC
Q_{gs}	Gate-Source Charge	$V_{\text{DS}}=15\text{V}, I_{\text{D}}=7.5\text{A}$, $V_{\text{GS}}=10\text{V}$		1.2		nC
Q_{gd}	Gate-Drain Charge			2.5		nC
DRAIN-SOURCE DIODE CHARACTERISTICS						
I_{s}	Maximum Continuous Drain-Source Diode Forward Current			1.3		A
V_{SD}	Diode Forward Voltage ^b	$V_{\text{GS}}=0\text{V}, I_{\text{s}}=1.3\text{A}$		0.78	1.3	V

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P-Channel ELECTRICAL CHARACTERISTICS ($T_A=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
OFF CHARACTERISTICS						
BV_{DSS}	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}$, $\text{I}_{\text{D}}=-250\mu\text{A}$	-30			V
I_{DSS}	Zero Gate Voltage Drain Current	$\text{V}_{\text{DS}}=-24\text{V}$, $\text{V}_{\text{GS}}=0\text{V}$			-1	μA
I_{GSS}	Gate-Body Leakage Current	$\text{V}_{\text{GS}}= \pm 20\text{V}$, $\text{V}_{\text{DS}}=0\text{V}$			± 10	μA
ON CHARACTERISTICS						
$\text{V}_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}$, $\text{I}_{\text{D}}=-250\mu\text{A}$	-1.0	-1.8	-3.0	V
$\text{R}_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance	$\text{V}_{\text{GS}}=-10\text{V}$, $\text{I}_{\text{D}}=-6.0\text{A}$		28	35	m ohm
		$\text{V}_{\text{GS}}=-4.5\text{V}$, $\text{I}_{\text{D}}=-4.8\text{A}$		42	55	m ohm
g_{FS}	Forward Transconductance	$\text{V}_{\text{DS}}=-10\text{V}$, $\text{I}_{\text{D}}=-3.0\text{A}$		8.5		S
DYNAMIC CHARACTERISTICS ^c						
C_{iss}	Input Capacitance	$\text{V}_{\text{DS}}=-15\text{V}, \text{V}_{\text{GS}}=0\text{V}$ $f=1.0\text{MHz}$		860		pF
C_{oss}	Output Capacitance			225		pF
C_{rss}	Reverse Transfer Capacitance			135		pF
SWITCHING CHARACTERISTICS ^c						
$t_{\text{D}(\text{ON})}$	Turn-On Delay Time	$\text{V}_{\text{DD}}=-15\text{V}$ $\text{I}_{\text{D}}=-1\text{A}$ $\text{V}_{\text{GS}}=-10\text{V}$ $\text{R}_{\text{GEN}}=6\text{ ohm}$		15		ns
t_{r}	Rise Time			18		ns
$t_{\text{D}(\text{OFF})}$	Turn-Off Delay Time			63		ns
t_{f}	Fall Time			15		ns
Q_g	Total Gate Charge	$\text{V}_{\text{DS}}=-15\text{V}, \text{I}_{\text{D}}=-6.0\text{A}, \text{V}_{\text{GS}}=-10\text{V}$		16.8		nC
		$\text{V}_{\text{DS}}=-15\text{V}, \text{I}_{\text{D}}=-6.0\text{A}, \text{V}_{\text{GS}}=-4.5\text{V}$		8.5		nC
Q_{gs}	Gate-Source Charge	$\text{V}_{\text{DS}}=-15\text{V}, \text{I}_{\text{D}}=-6.0\text{A},$ $\text{V}_{\text{GS}}=-10\text{V}$		1.7		nC
Q_{gd}	Gate-Drain Charge			3.5		nC
DRAIN-SOURCE DIODE CHARACTERISTICS						
I_s	Maximum Continuous Drain-Source Diode Forward Current				-1.2	A
V_{SD}	Diode Forward Voltage ^b	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_{\text{S}}=-1.2\text{A}$		-0.76	-1.2	V
Notes						
<p>a.Surface Mounted on FR4 Board,$t \leq 10\text{sec}$.</p> <p>b.Pulse Test:Pulse Width $\leq 300\text{us}$, Duty Cycle $\leq 2\%$.</p> <p>c.Guaranteed by design, not subject to production testing.</p> <p>d.Starting $T_J=25^\circ\text{C}$, $\text{V}_{\text{DD}}=20\text{V}$, $\text{V}_{\text{GS}}=10\text{V}$, $L=0.5\text{mH}$.</p>						

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N-Channel

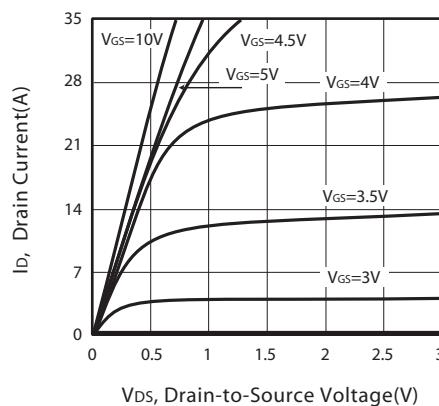


Figure 1. Output Characteristics

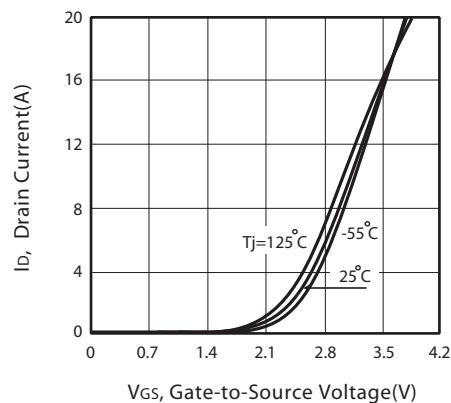


Figure 2. Transfer Characteristics

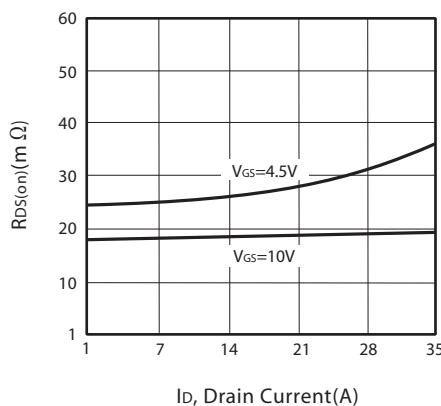


Figure 3. On-Resistance vs. Drain Current and Gate Voltage

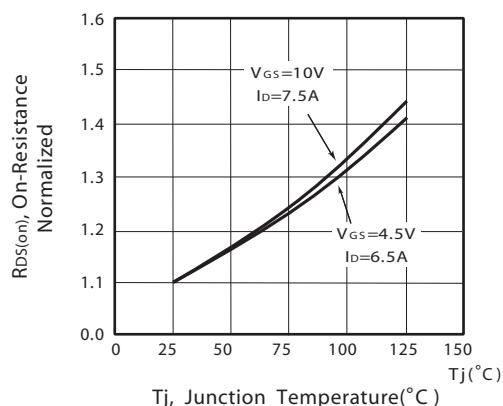


Figure 4. On-Resistance Variation with Drain Current and Temperature

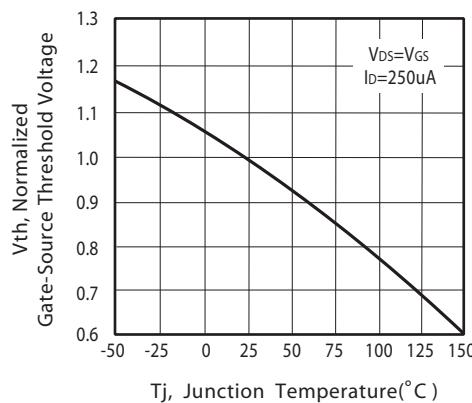


Figure 5. Gate Threshold Variation with Temperature

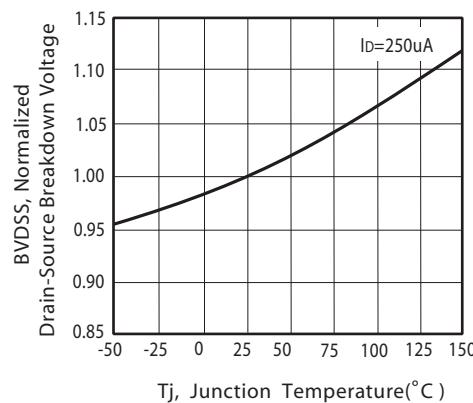
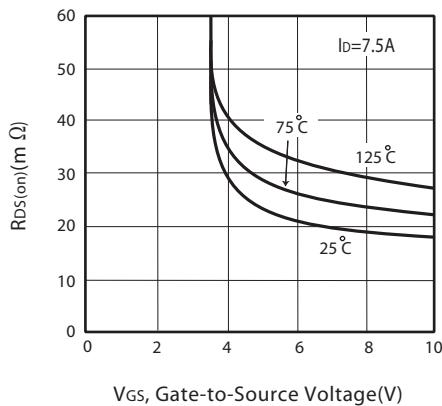


Figure 6. Breakdown Voltage Variation with Temperature

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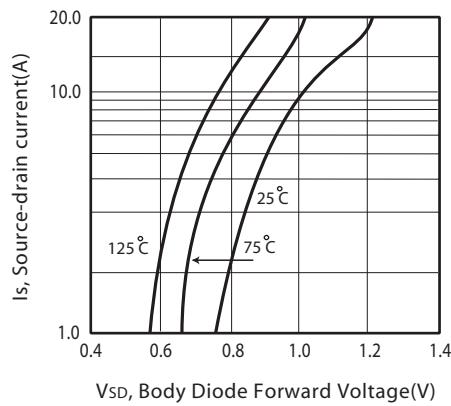
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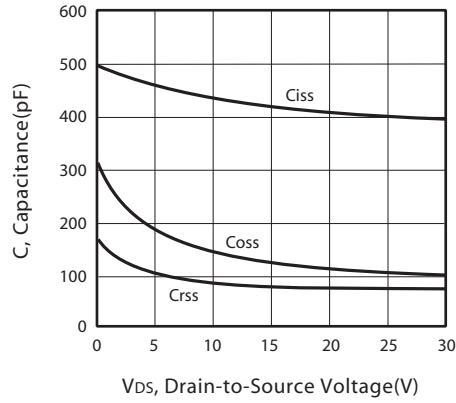
V_{GS}, Gate-to-Source Voltage(V)

Figure 7. On-Resistance vs. Gate-Source Voltage



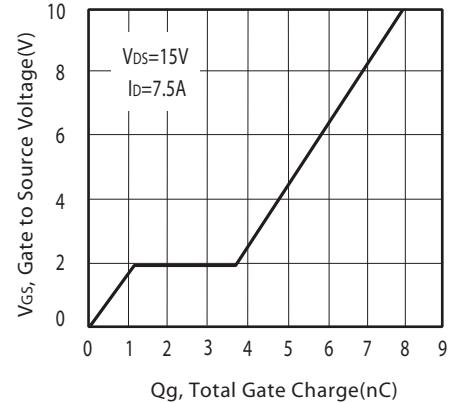
V_{SD}, Body Diode Forward Voltage(V)

Figure 8. Body Diode Forward Voltage Variation with Source Current



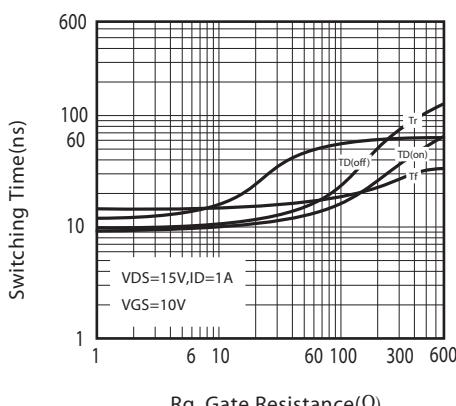
V_{DS}, Drain-to-Source Voltage(V)

Figure 9. Capacitance



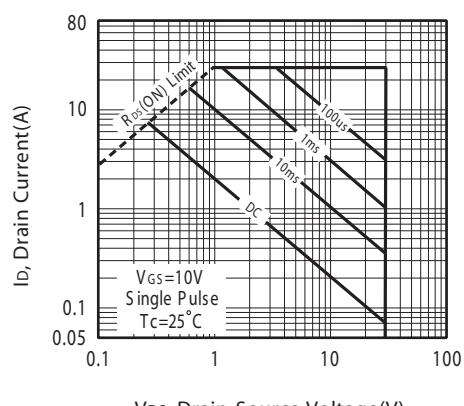
Q_g, Total Gate Charge(nC)

Figure 10. Gate Charge



R_g, Gate Resistance(Ω)

Figure 11. switching characteristics

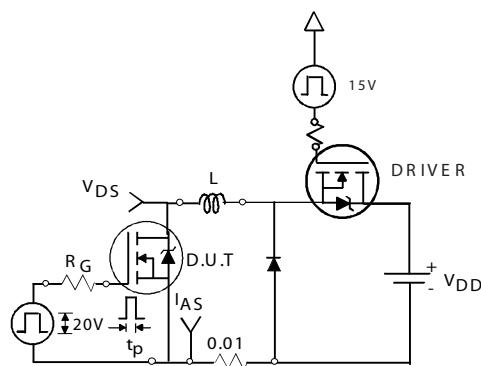


I_D, Drain Current(A)

Figure 12. Maximum Safe Operating Area

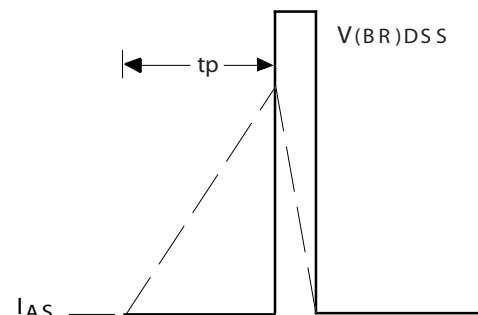
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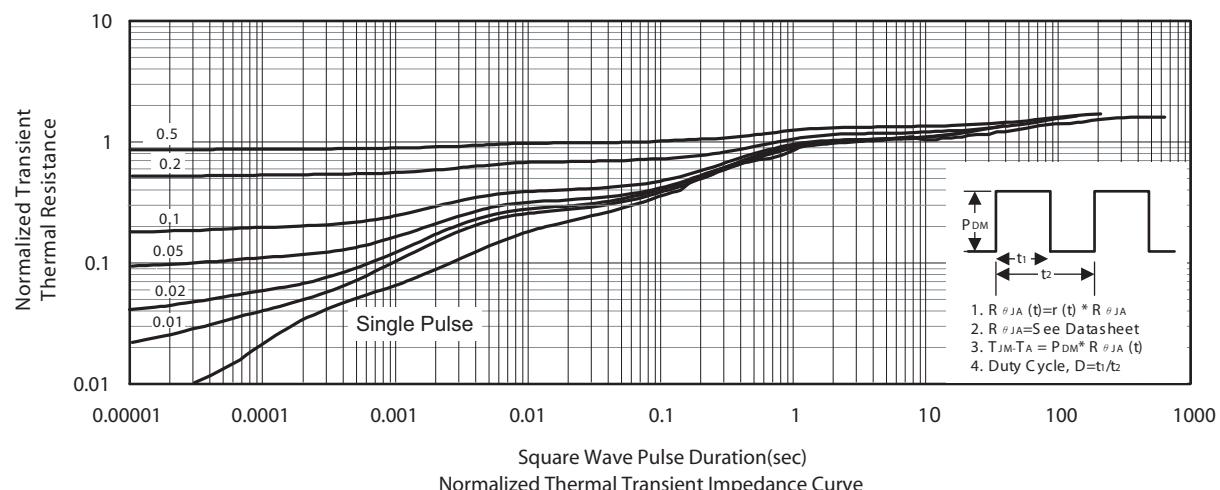
Unclamped Inductive Test Circuit

Figure 13a.



Unclamped Inductive Waveforms

Figure 13b.



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P-Channel

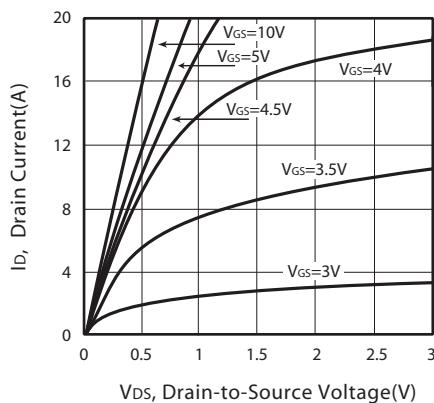


Figure 1. Output Characteristics

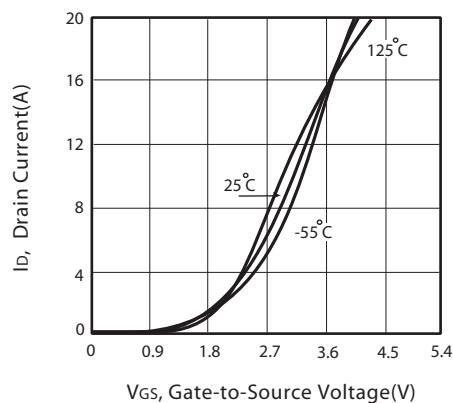


Figure 2. Transfer Characteristics

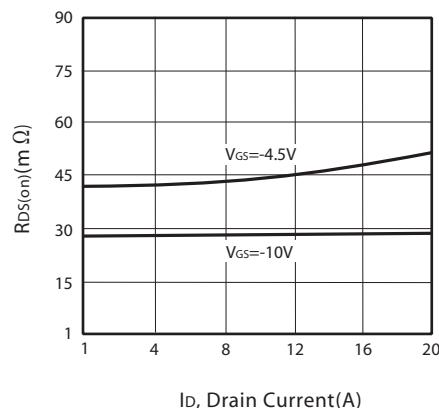


Figure 3. On-Resistance vs. Drain Current and Gate Voltage

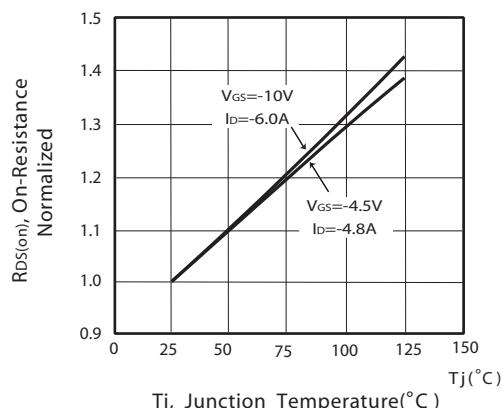


Figure 4. On-Resistance Variation with Drain Current and Temperature

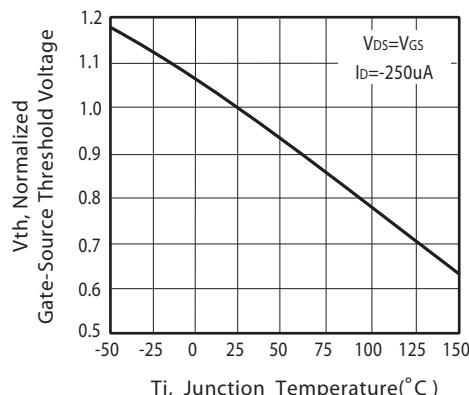


Figure 5. Gate Threshold Variation with Temperature

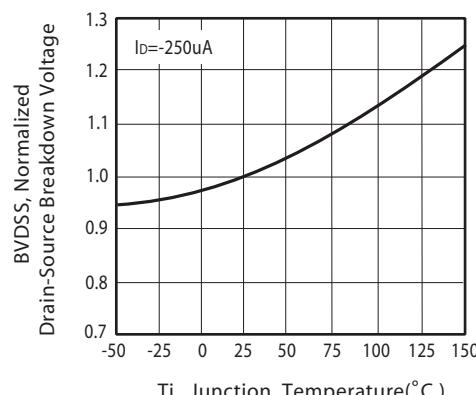
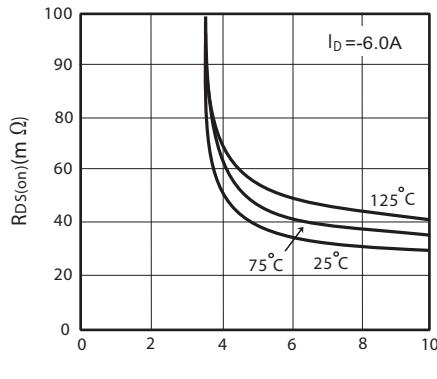


Figure 6. Breakdown Voltage Variation with Temperature

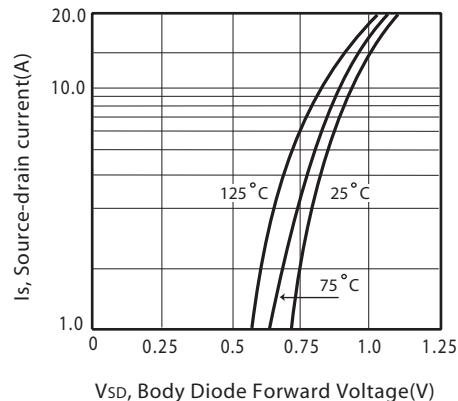
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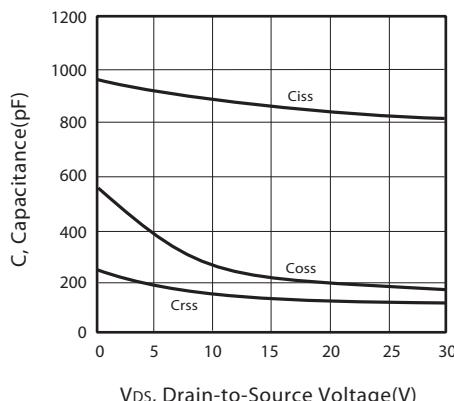
V_{GS}, Gate-to-Source Voltage(V)

Figure 7. On-Resistance vs.
Gate-Source Voltage



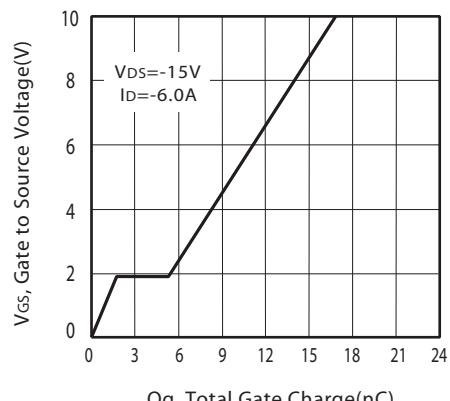
V_{SD}, Body Diode Forward Voltage(V)

Figure 8. Body Diode Forward Voltage
Variation with Source Current



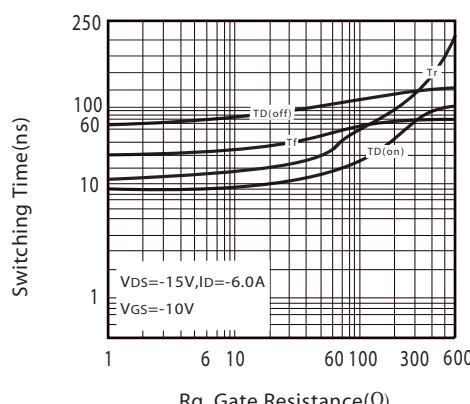
V_{DS}, Drain-to-Source Voltage(V)

Figure 9. Capacitance



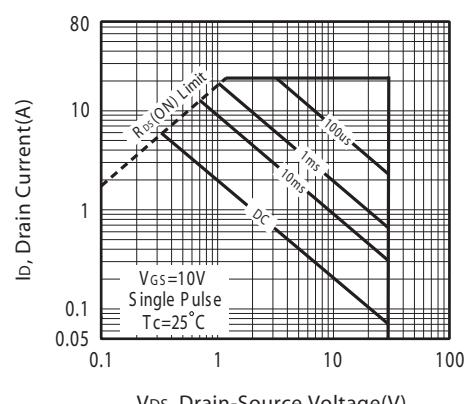
Q_g, Total Gate Charge(nC)

Figure 10. Gate Charge



R_g, Gate Resistance(Ω)

Figure 11. switching characteristics

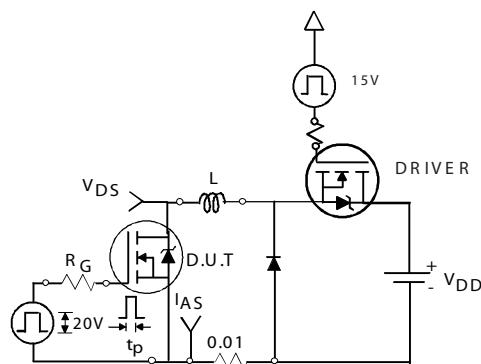


I_D, Drain Current(A)
V_{DS}, Drain-Source Voltage(V)

Figure 12. Maximum Safe Operating Area

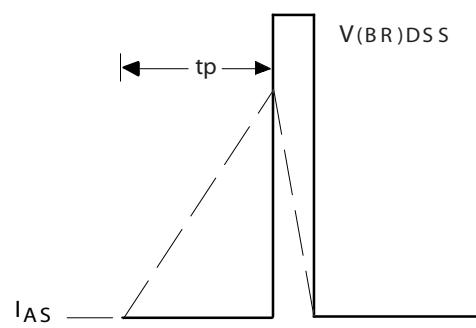
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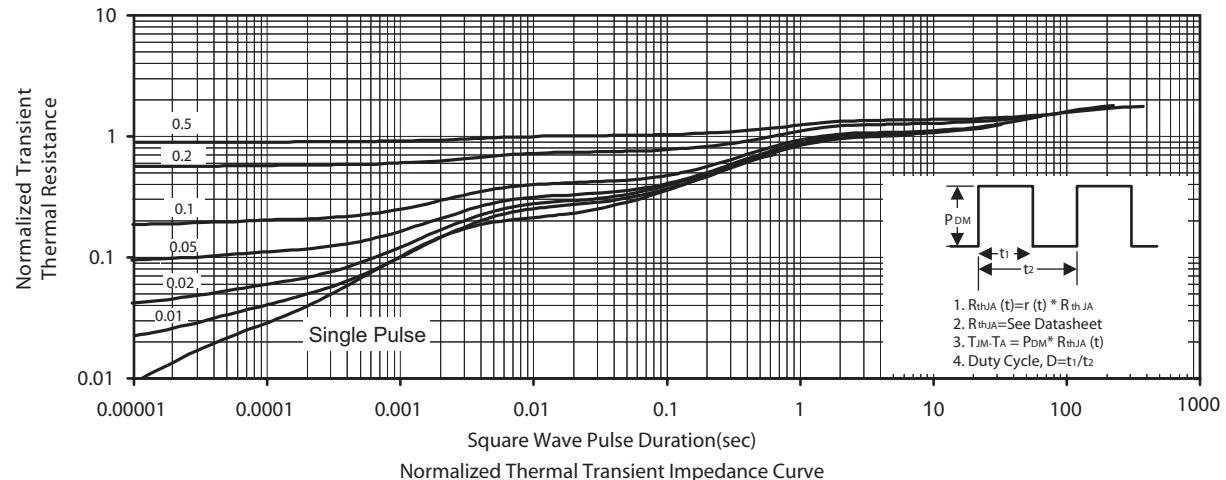
Unclamped Inductive Test Circuit

Figure 13a.



Unclamped Inductive Waveforms

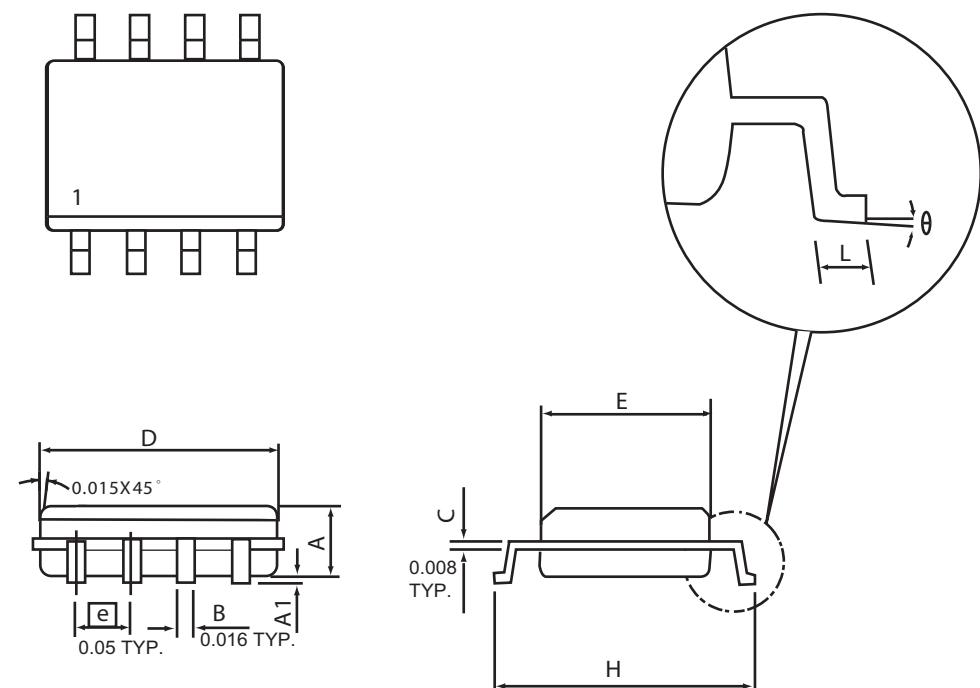
Figure 13b.



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PACKAGE OUTLINE DIMENSIONS

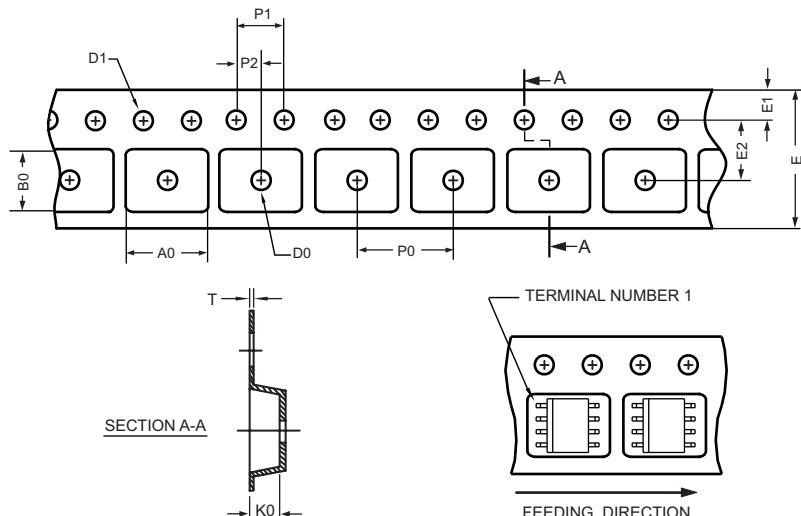
SO-8



SYMBOLS	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	1.35	1.75	0.053	0.069
A1	0.10	0.25	0.004	0.010
D	4.80	4.98	0.189	0.196
E	3.81	3.99	0.150	0.157
H	5.79	6.20	0.228	0.244
L	0.41	1.27	0.016	0.050
θ	0°	8°	0°	8°

SO-8 Tape and Reel Data

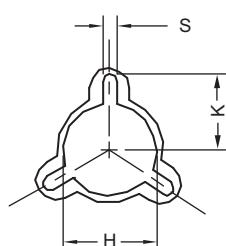
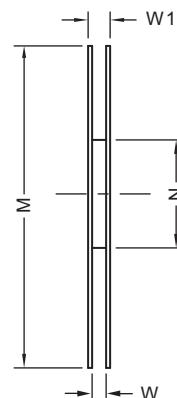
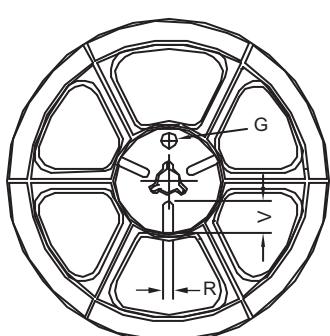
SO-8 Carrier Tape



unit:mm

PACKAGE	A0	B0	K0	D0	D1	E	E1	E2	P0	P1	P2	T
SOP 8N 150mil	6.50 ± 0.15	5.25 ± 0.10	2.10 ± 0.10	$\phi 1.5$ (MIN)	$\phi 1.55$ ± 0.10	12.0 $+0.3$ -0.1	1.75 ± 0.10	5.5 ± 0.10	8.0 ± 0.10	4.0 ± 0.10	2.0 ± 0.10	0.30 ± 0.013

SO-8 Reel



UNIT:mm

TAPE SIZE	REEL SIZE	M	N	W	W1	H	K	S	G	R	V
12 mm	$\phi 330$	330 ± 1	62 ± 1.5	12.4 $+0.2$	16.8 -0.4	$\phi 12.75$ $+0.15$	---	2.0 ± 0.15	---	---	---