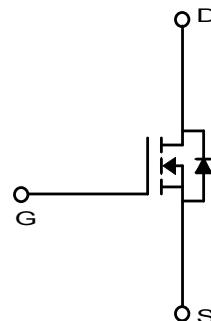
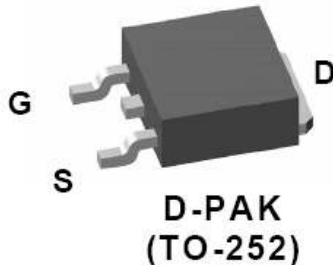


General Description

The MDD1502 uses advanced MagnaChip's MOSFET Technology, which provides high performance in on-state resistance, fast switching performance and excellent quality. MDD1502 is suitable device for DC to DC converter and general purpose applications.

Features

- $V_{DS} = 30V$
- $I_D = 45.7A @ V_{GS} = 10V$
- $R_{DS(ON)}(\text{MAX}) < 8.5m\Omega @ V_{GS} = 10V$
- $< 13.0m\Omega @ V_{GS} = 4.5V$
- 100% UIL Tested
- 100% R_g Tested



Absolute Maximum Ratings ($T_a = 25^\circ C$)

Characteristics	Symbol	Rating	Unit
Drain-Source Voltage	V_{DSS}	30	V
Gate-Source Voltage	V_{GSS}	± 20	V
Continuous Drain Current ⁽¹⁾	I_D	45.7	A
		36.6	
		20.4 ⁽³⁾	
		16.3 ⁽³⁾	
Pulsed Drain Current	I_{DM}	100	A
Power Dissipation	P_D	31.2	W
		20.0	
		6.2 ⁽³⁾	
		4.0 ⁽³⁾	
Single Pulse Avalanche Energy ⁽²⁾	E_{AS}	47	mJ
Junction and Storage Temperature Range	T_J, T_{stg}	-55~150	°C

Thermal Characteristics

Characteristics	Symbol	Rating	Unit
Thermal Resistance, Junction-to-Ambient ⁽¹⁾	$R_{\theta JA}$	20.0	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	4.0	

Ordering Information

Part Number	Temp. Range	Package	Packing	Quantity	Rohs Status
MDD1502RH	-55~150°C	D-PAK	Tape & Reel	3000 units	Halogen Free

Electrical Characteristics ($T_J = 25^\circ\text{C}$)

Characteristics	Symbol	Test Condition	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$I_D = 250\mu\text{A}, V_{GS} = 0\text{V}$	30	-	-	V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1.6	2.0	2.7	
Drain Cut-Off Current	$I_{DS(on)}$	$V_{DS} = 30\text{V}, V_{GS} = 0\text{V}$ $T_J = 55^\circ\text{C}$	-	-	1	μA
Gate Leakage Current	I_{GSS}	$V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$	-	-	± 0.1	
Drain-Source ON Resistance	$R_{DS(on)}$	$V_{GS} = 10\text{V}, I_D = 16\text{A}$ $T_J = 125^\circ\text{C}$	-	7.4	8.5	$\text{m}\Omega$
		$V_{GS} = 4.5\text{V}, I_D = 13\text{A}$	-	10.8	13.0	
Forward Transconductance	g_{fs}	$V_{DS} = 5\text{V}, I_D = 10\text{A}$	-	25	-	S
Dynamic Characteristics						
Total Gate Charge	$Q_{g(10\text{V})}$	$V_{DS} = 15.0\text{V}, I_D = 16\text{A}, V_{GS} = 10\text{V}$	10.7	14.3	17.9	nC
Total Gate Charge	$Q_{g(4.5\text{V})}$		5.0	6.7	8.4	
Gate-Source Charge	Q_{gs}		-	2.6	-	
Gate-Drain Charge	Q_{gd}		-	2.3	-	
Input Capacitance	C_{iss}	$V_{DS} = 15.0\text{V}, V_{GS} = 0\text{V}, f = 1.0\text{MHz}$	696	928	1160	pF
Reverse Transfer Capacitance	C_{rss}		68	90	113	
Output Capacitance	C_{oss}		132	176	220	
Turn-On Delay Time	$t_{d(on)}$	$V_{GS} = 10\text{V}, V_{DS} = 15.0\text{V}, I_D = 16\text{A}, R_G = 3.0\Omega$	-	7.2	-	ns
Rise Time	t_r		-	12.0	-	
Turn-Off Delay Time	$t_{d(off)}$		-	22.8	-	
Fall Time	t_f		-	8.1	-	
Gate Resistance	R_g	$f=1\text{ MHz}$	-	3.5	5.0	Ω
Drain-Source Body Diode Characteristics						
Source-Drain Diode Forward Voltage	V_{SD}	$I_S = 16\text{A}, V_{GS} = 0\text{V}$	-	0.8	1.1	V
Body Diode Reverse Recovery Time	t_{rr}	$I_F = 16\text{A}, dI/dt = 100\text{A}/\mu\text{s}$	-	20.4	30.6	ns
Body Diode Reverse Recovery Charge	Q_{rr}		-	11.9	17.9	nC

Note :

1. Surface mounted FR-4 board by JEDEC (jesd51-7)
2. E_{AS} is tested at starting $T_j = 25^\circ\text{C}$, $L = 0.1\text{mH}$, $I_{AS} = 17.0\text{A}$, $V_{DD} = 27\text{V}$, $V_{GS} = 10\text{V}$.
3. $T < 10\text{sec}$.

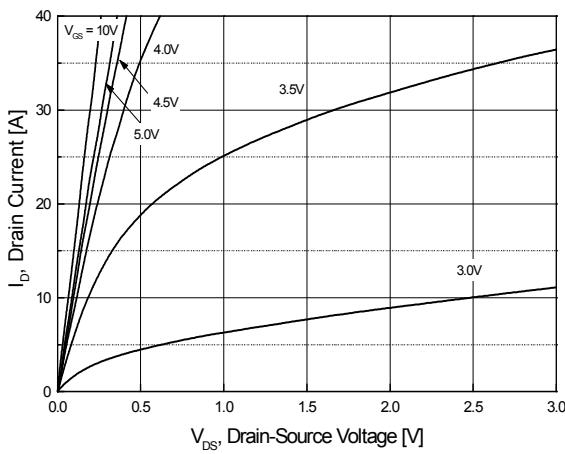


Fig.1 On-Region Characteristics

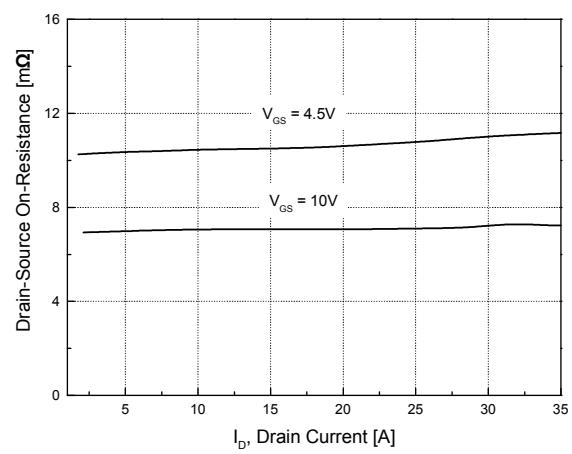


Fig.2 On-Resistance Variation with Drain Current and Gate Voltage

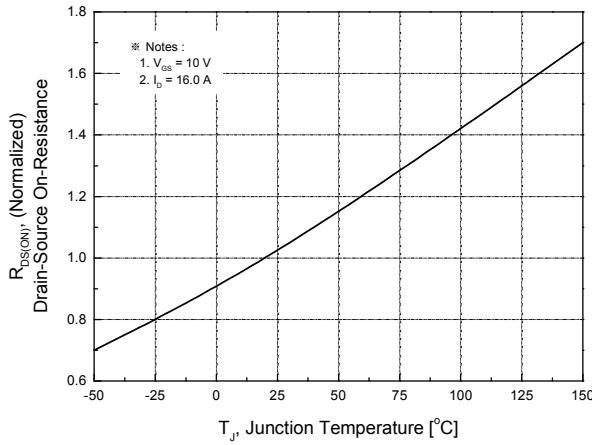


Fig.3 On-Resistance Variation with Temperature

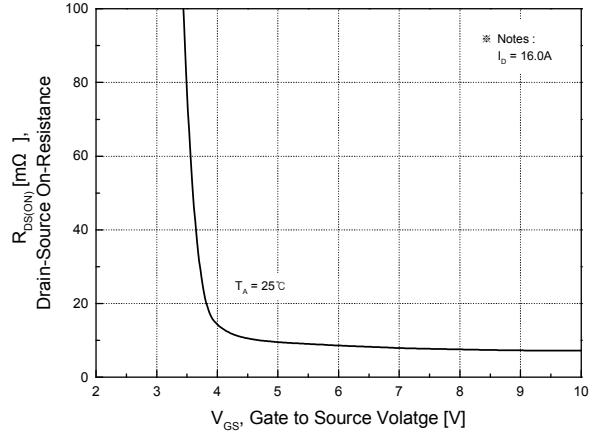


Fig.4 On-Resistance Variation with Gate to Source Voltage

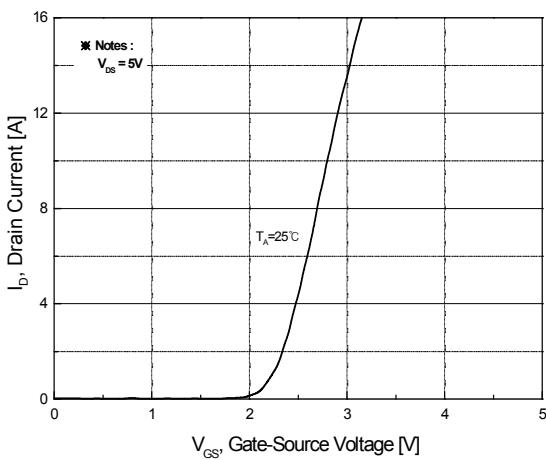


Fig.5 Transfer Characteristics

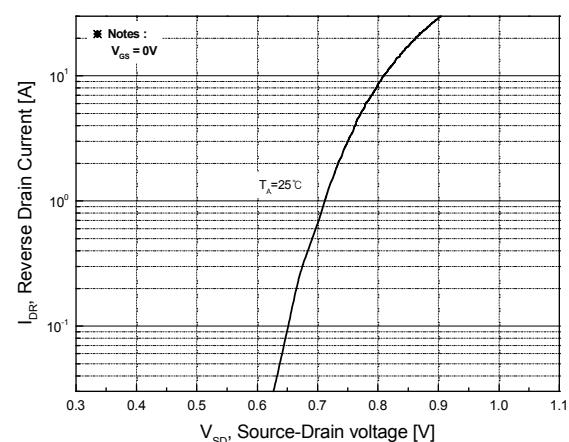


Fig.6 Body Diode Forward Voltage Variation with Source Current and Temperature

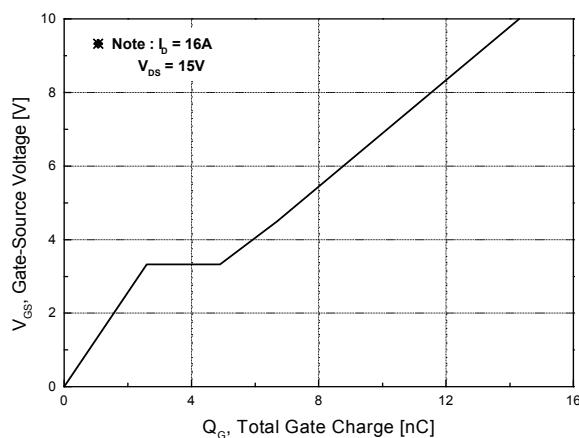


Fig.7 Gate Charge Characteristics

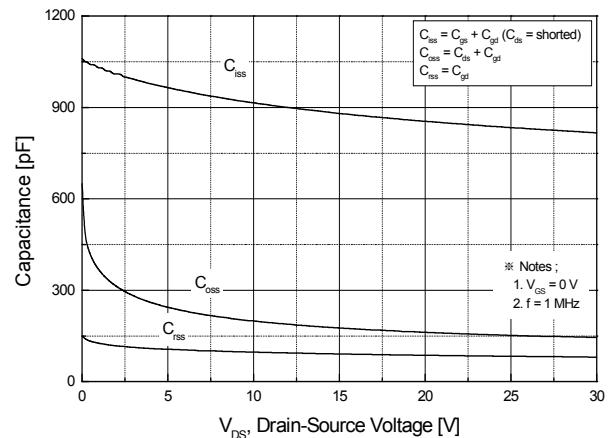


Fig.8 Capacitance Characteristics

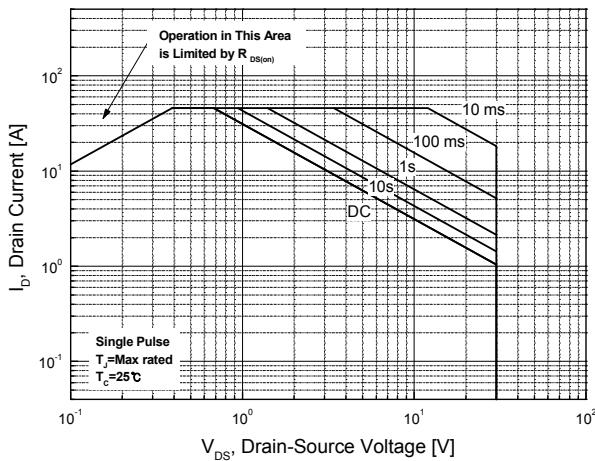


Fig.9 Maximum Safe Operating Area

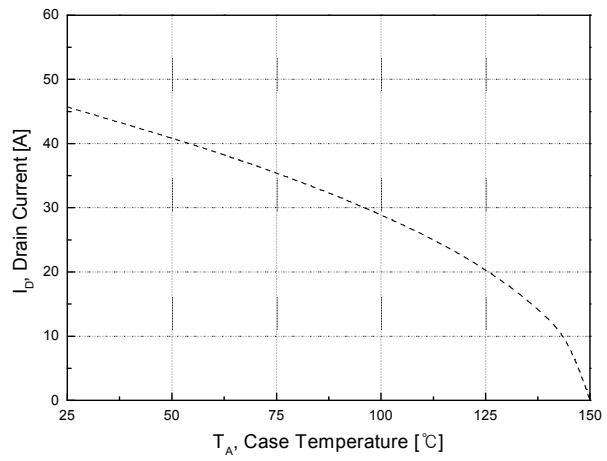


Fig.10 Maximum Drain Current vs. Case Temperature

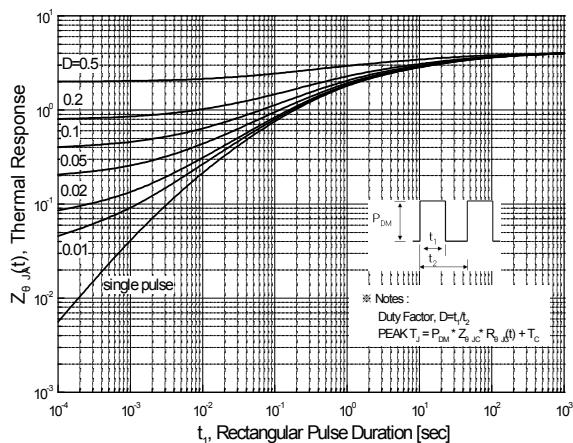
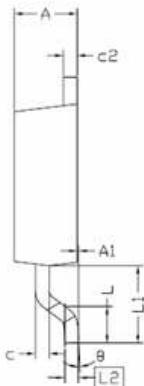
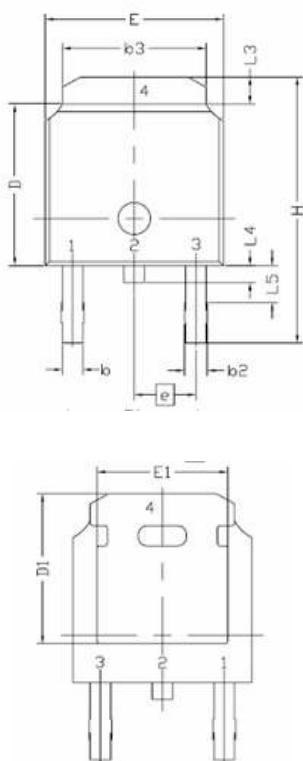


Fig.11 Transient Thermal Response Curve

Package Dimension

D-PAK (TO-252)

Dimensions are in millimeters, unless otherwise specified



Symbol	Min.	Nom.	Max.
E	6.35	-	6.73
L	1.40	1.52	1.78
L1		2.74 REF	
L2		0.508 BCS	
L3	0.89	-	1.27
L4	-	-	1.02
L5	1.14	-	1.52
D	5.97	6.10	6.22
H	9.40	-	10.41
b	0.64	-	0.89
b2	0.76	-	1.14
b3	4.95	-	5.46
e		2.286 BSC	
A	2.18	-	2.39
A1	-	-	0.13
c	0.46	-	0.61
c2	0.46	-	0.89
D1	5.21	-	-
E1	4.32	-	-
θ	0.00	-	10.00

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