RoHS Compliant
HALOGEN-FREE

MagnaChip

MDS3652

Single P-Channel Trench MOSFET, -30V, -11A, 17mΩ

General Description

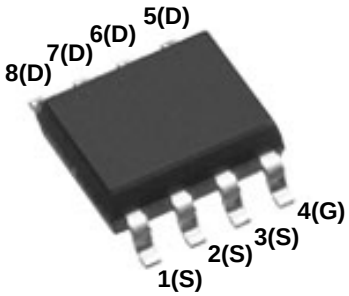
The MDS3652 uses advanced MagnaChip's MOSFET Technology to provide low on-state resistance, high switching performance and excellent reliability

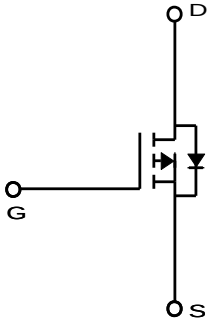
Features

- V_{DS} = -30V
- I_D = -11A @V_{GS} = -10V
- $R_{DS(ON)}$
  < 17mΩ @V_{GS} = -10V
  < 27mΩ @V_{GS} = -4.5V

Applications

- Load Switch
- General purpose applications





Absolute Maximum Ratings (T_a =25°C unless otherwise noted)

Characteristics		Symbol	Rating	Unit
Drain-Source Voltage		V_{DSS}	-30	V
Gate-Source Voltage		V_{GSS}	±20	V
Continuous Drain Current	T_a=25°C	I_D	-11	A
	T_a=100°C		-7	A
Pulsed Drain Current		I_{DM}	-60	A
Power Dissipation(1)	T_a=25°C	P_D	3.1	W
	T_a=100°C		1.2	
Single Pulse Avalanche Energy(2)		E_{AS}	60	mJ
Junction and Storage Temperature Range		T_J, T_{stg}	-55~150	°C

Thermal Characteristics

Characteristics	Symbol	Rating	Unit
Thermal Resistance, Junction-to-Ambient(Steady-State)(1)	$R_{\theta JA}$	40	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	25	

Ordering Information

Part Number	Temp. Range	Package	Packing	RoHS Status
MDS3652URH	-55~150°C	SOIC-8	Tape & Reel	Halogen Free

Electrical Characteristics (T_a = 25°C unless otherwise noted)

Characteristics	Symbol	Test Condition	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	I_D = -250μA, V_{GS} = 0V	-30	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = -250μA	-1.0	-1.9	-3.0	
Drain Cut-Off Current	I_{DSS}	V_{DS} = -24V, V_{GS} = 0V	-		-1	μA
Gate Leakage Current	I_{GSS}	V_{GS} = ±20V, V_{DS} = 0V	-	-	±0.1	
Drain-Source ON Resistance	$R_{DS(ON)}$	V_{GS} = -10V, I_D = -11A	-	13	17	mΩ
		V_{GS} = -4.5V, I_D = -6A	-	21	27	
Forward Transconductance	g_{FS}	V_{DS} = -5V, I_D = -11A	-	25	-	S
Dynamic Characteristics						
Total Gate Charge	Q_g	V_{DS} = -15V, I_D = -11A, V_{GS} = -10V	-	35	-	nC
Gate-Source Charge	Q_{gs}		-	7.8	-	
Gate-Drain Charge	Q_{gd}		-	6.2	-	
Input Capacitance	C_{iss}	V_{DS} = -15V, V_{GS} = 0V, f = 1.0MHz	-	1770	-	pF
Reverse Transfer Capacitance	C_{rss}		-	150	-	
Output Capacitance	C_{oss}		-	350	-	
Turn-On Delay Time	$t_{d(on)}$	V_{GS} = -10V ,V_{DS} = -15V, R_L = 2.7Ω, R_{GEN} = 3Ω	-	13.0	-	ns
Turn-On Rise Time	t_r		-	26.8	-	
Turn-Off Delay Time	$t_{d(off)}$		-	34.4	-	
Turn-Off Fall Time	t_f		-	17.4	-	
Drain-Source Body Diode Characteristics						
Source-Drain Diode Forward Voltage	V_{SD}	I_S = 1A, V_{GS} = 0V	-	-0.75	-	V
Body Diode Reverse Recovery Time	t_{rr}	I_F = -11A, di/dt = 100A/μs	-	27	-	ns
Body Diode Reverse Recovery Charge	Q_{rr}		-	12	-	nC

Note :

1. Surface mounted FR-4 board by JEDEC (jesd51-7)
2. Starting T_J = 25°C, L = 1mH, I_{AS} = 11A, V_{DD} = 15V, V_{GS} = 10V

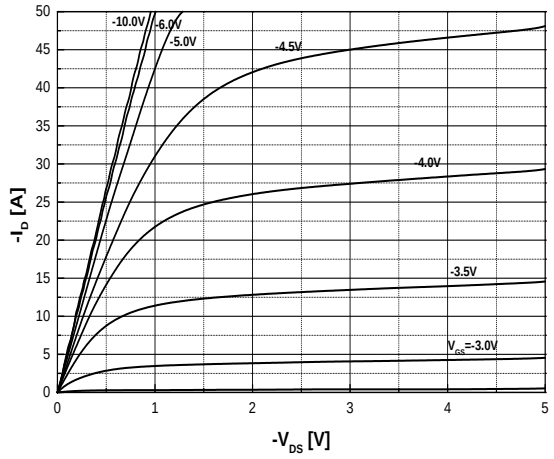


Fig.1 On-Region Characteristics

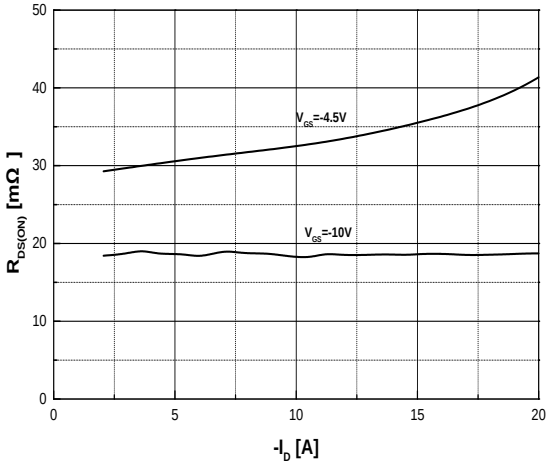


Fig.2 On-Resistance Variation with Drain Current and Gate Voltage

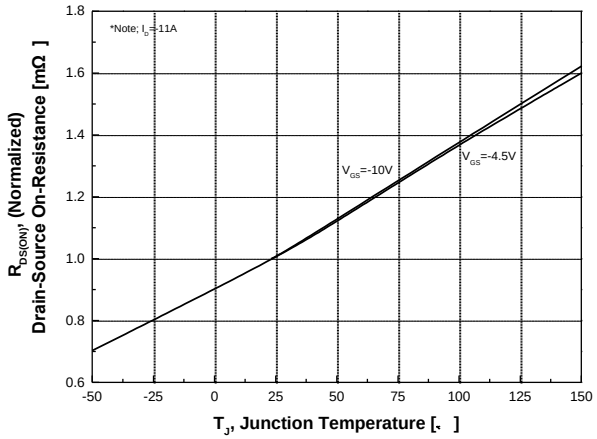


Fig.3 On-Resistance Variation with Temperature

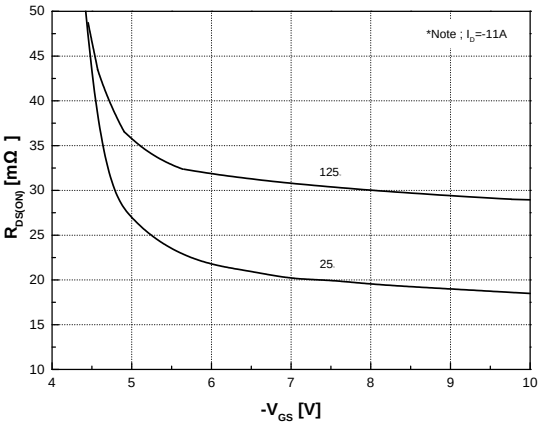


Fig.4 On-Resistance Variation with Gate to Source Voltage

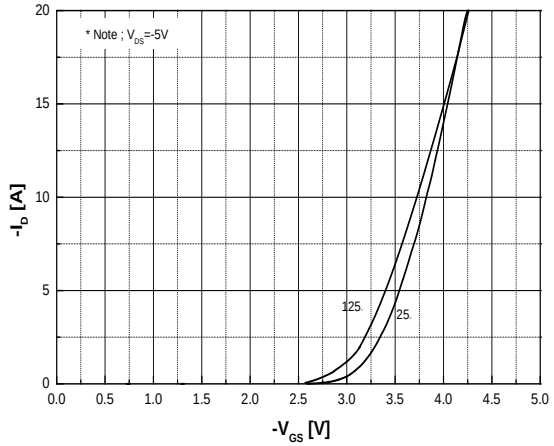


Fig.5 Transfer Characteristics

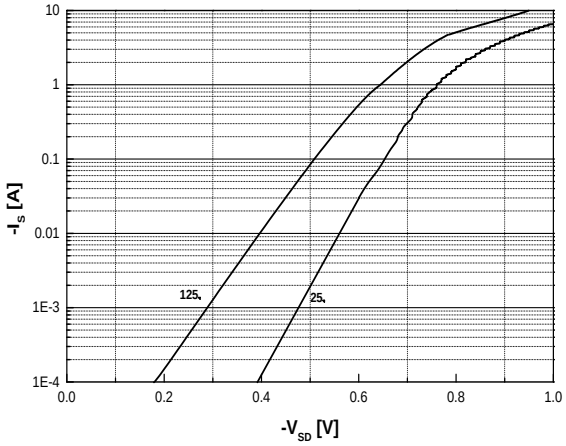


Fig.6 Body Diode Forward Voltage Variation with Source Current and Temperature

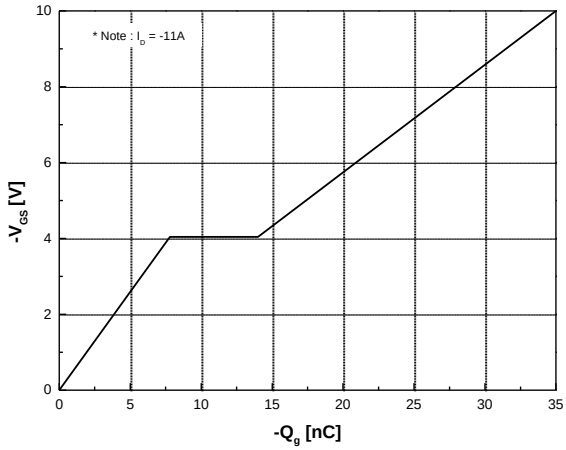


Fig.7 Gate Charge Characteristics

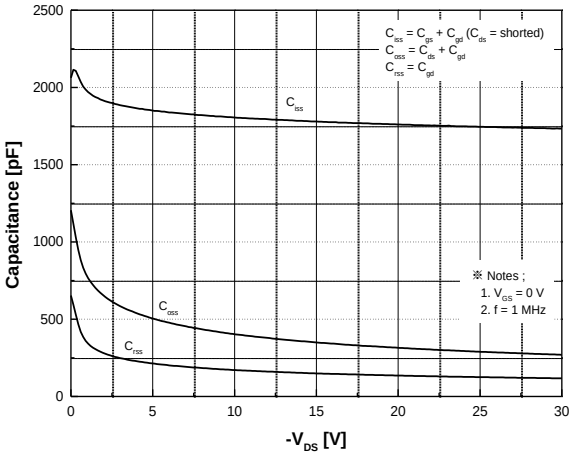


Fig.8 Capacitance Characteristics

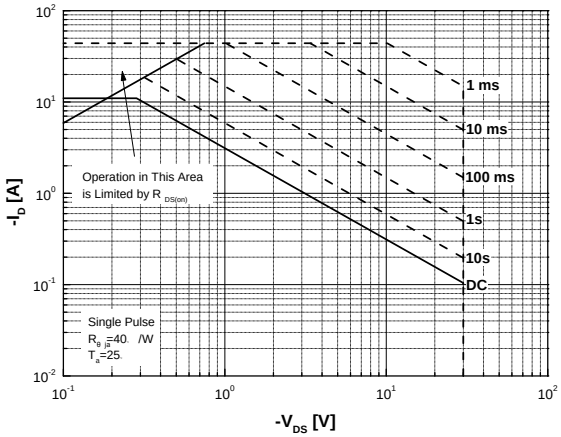


Fig.9 Maximum Safe Operating Area

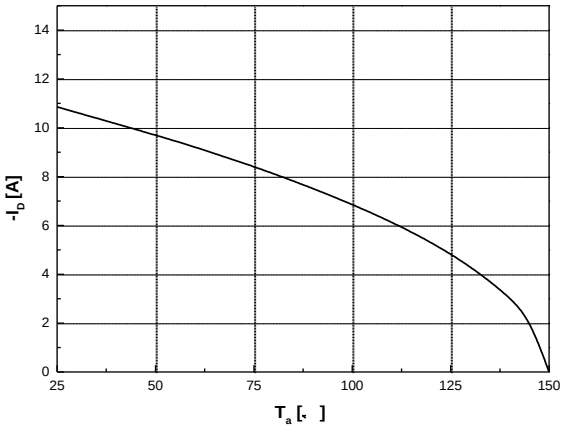


Fig.10 Maximum Drain Current vs. Ambient Temperature

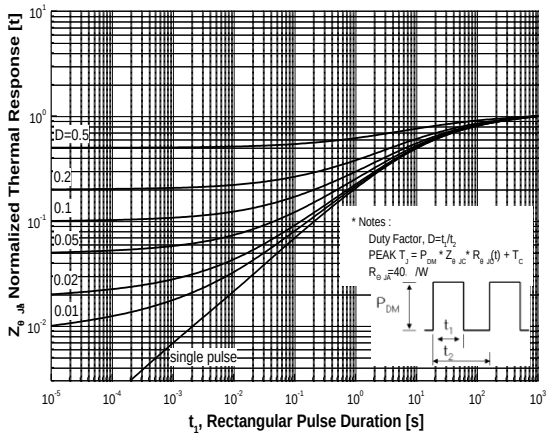
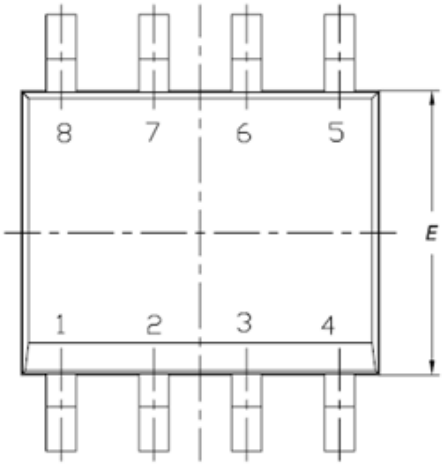


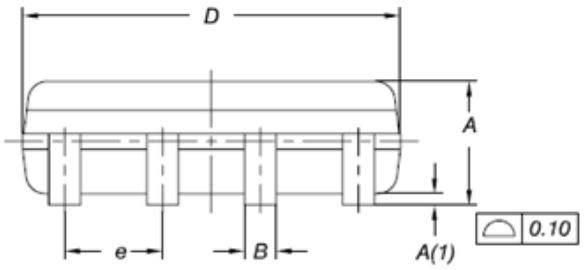
Fig.11 Transient Thermal Response Curve

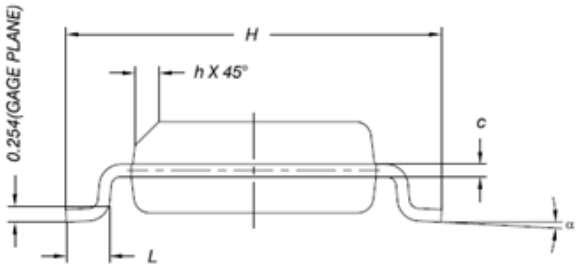
8 Leads, SOIC

Dimensions are in millimeters unless otherwise specified



Symbol	Min	Nom	Max
A	-	-	1.75
A(1)	0.10	-	0.25
B	0.31	-	0.51
C	0.10	-	0.25
D	4.9 BSC		
E	3.9 BSC		
e	1.27 BSC		
H	6.0 BSC		
L	0.40	-	1.27
a	0	-	8
h	0.250	-	0.500
L2(Gage plane)	0.25 BSC		





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